

# I/O PIN ASSIGNMENTS

**AC INPUT: DBM-C5H5PJ K87**

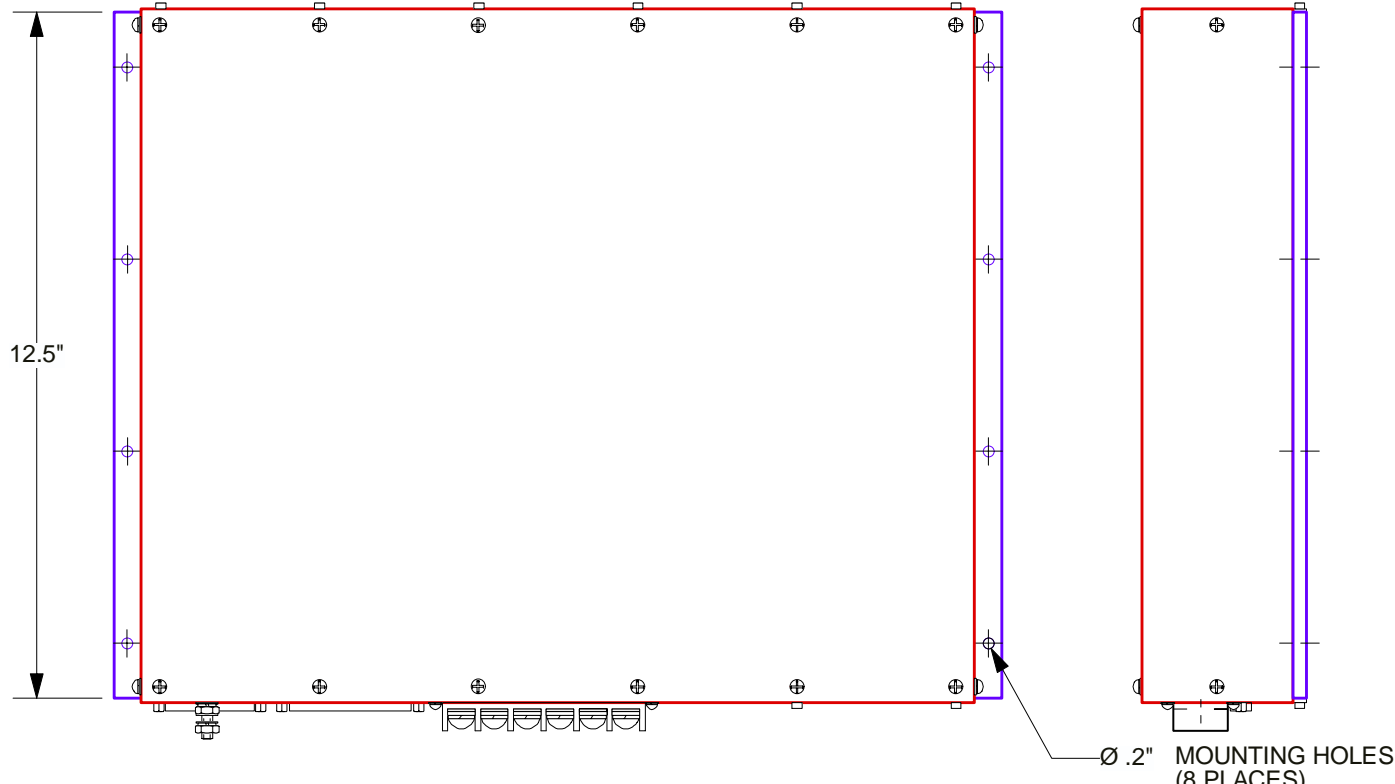
JA - 1	115VAC
JA - 2	115VAC
JA - 3	115VAC
JA - 4	CHASSIS GND
JA - 5	CHASSIS GND

**DC OUT (DIGITAL): DCM-37ST A197**

JB - 1	+12V
JB - 2	+12V DGND
JB - 3	-.25V DGND
JB - 4	-12V
JB - 5	+15VX
JB - 6	+15VX DGND
JB - 7	+48V
JB - 8	+48V DGND
JB - 9	BITE
JB - 10	+5VX SENSE
JB - 11	+5VX DGND SENSE
JB - 12	-2V DGND SENSE
JB - 13	-2V SENSE
JB - 14	-4.5V DGND SENSE
JB - 15	-4.5V SENSE
JB - 16	(spare)
JB - 17	(spare)
JB - 18	+15 VXF
JB - 19	+15 VXF DGND
JB - 20	+12V
JB - 21	+12V DGND
JB - 22	-12V DGND
JB - 23	12V
JB - 24	+15VX
JB - 25	+15VX DGND
JB - 26	+48V
JB - 27	+48V DGND
JB - 28	+5VX SENSE
JB - 29	+5VX DGND SENSE
JB - 30	-2V DGND SENSE
JB - 31	-2V SENSE
JB - 32	-4.5V DGND SENSE
JB - 33	-4.5V SENSE
JB - 34	BIT SYSFAIL
JB - 35	BIT ACFAIL
JB - 36	BIT SYSRESET
JB - 37	INHIBIT (Input)

**DC OUT (ANALOG): DDM-24W7S A197**

JC - A1	+5.0V
JC - A2	+5.0V AGND
JC - A3	-5.2V AGND
JC - A4	-5.2V
JC - A5	+15VY
JC - A6	+15VY / -15VZ AGND
JC - A7	-15VZ
JC - 1	(spare)
JC - 2	(spare)
JC - 3	(spare)
JC - 4	+20V
JC - 5	+20V AGND
JC - 6	+20V AGND
JC - 7	+20V
JC - 8	(spare)
JC - 9	+15VY
JC - 10	+15VY AGND
JC - 11	15VZ AGND
JC - 12	-15VZ
JC - 13	(spare)
JC - 14	(spare)
JC - 15	(spare)
JC - 16	(spare)
JC - 17	(spare)



**POST CONNECTORS**

E1	CHASSIS GND
E2	-2V DGND
E3	-2V
E4	-4.5V DGND
E5	-4.5V
E6	+5VX
E7	+5VX DGND

**WEIGHT:** < 17.4 lb. (< 7.9 kg.)

**3AJØ3**

*Viable Power Conversion Technologies Inc.*

DRAWN: J.STEPIEN DATE: 08.JUN.98 CHKD: DATE: APPVD: DATE:	115V L-N 400Hz 3Ø <b>800W AC-DC PSU</b> -2V/54A, -4.5V/47A, +5VX/30A, +12V/1A, -12V/4A, +15VX/1.3A, +48V/1A, +5V/4A, -5.2V/3A, +15VY/3.2A, -15VZ/3.2A, +20V/1.1A	size <b>A</b>	Dwg.No. VOI 97011-00	Rev. <b>B</b>
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Scale 1:3.5 Sheet 1 of 1